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| Name of the Faculty & Photo |  BHARATHA SATHEESH | | |
| Designation | Assistant Professor | | |
| Qualification | M.Tech , PhD pursuing | | |
| Specialization | Embedded Systems | | |
| Total Experience | Teaching (Years) | Industry (Years) | Research (Years) |
| | 12 | ----- | 6 |
| Research Areas | | | Low power VLSI |
| Contact Details & mail ID | Mobile No : 9966590131 Email id : basateesh27@gmail.com | | |
| | | | |
| Subjects Taught | EDC,ECA,AE,PDC DCS | LDIC,STLD,DLD,MWE DSD | VLSI Design,FPGA &CPLD Architecture, |
| Projects guided | UG | PG | PHD |
| | 24 | 8 | --- |
| Publications | Journals | | Conferences |
| | 4 | | 4 |
| Publications List | Enclosed | | |
| Any other | Organizing various Work Shops based on VLSI | | |